What is claimed is:

[Claim 1] 1. A method for responding a reading command in an automatically adaptive memory card, comprising:

determining whether a present reading address of the reading command is same as a previous reading address,

wherein when said present reading address is same as said previous reading address;

outputting a response and outputting a data in a data-lag mode to output said data after said response is outputted,

wherein when said present reading address is not same as said previous reading address; and

updating said previous reading address to be said present reading address, output said response and outputting said data in a data-parallel mode to output said data regardless of whether or not said response is outputted.

[Claim 2] 2. The method of claim 1, wherein said step of outputting said data in a data-lag mode includes outputting said data after said response is outputted and after a first predetermined time passes.

[Claim 3] 3. The method of claim 2, further comprising a step of outputting said data in said data-lag mode during a second predetermined time without determining whether said present reading address of said reading command is the same as said previous reading address after said data is outputted in said data-lag mode.

[Claim 4] 4. The method of claim 3, further comprising a step of outputting said data in said data-lag mode without determining whether or not said present reading address of said reading command is the same as said previous reading address when it has determined for a plurality of times that said present reading address

of said reading command is the same as said previous reading address.

[Claim 5] 5. The method of claim 1, further comprising:

determining whether said reading command passes a cyclic redundancy check; and

stopping a step of processing said reading command when said reading command does not pass a cyclic redundancy check.

[Claim 6] 6. The method of claim 1, further comprising a step of reading said data based on said reading command from a memory of said memory card during said step of outputting said response.

[Claim 7] 7.The method of claim 1, wherein when said reading command is a multi-block reading command, further comprising:

outputting a data in a next block of said data after outputting said data; and stop outputting said data when said memory card receives a stop command.

[Claim 8] 8. The method of claim 1, wherein said memory card is a SD memory card.

[Claim 9] 9. A method for responding a reading command in an automatically adaptive memory card, comprising:

outputting a response, wherein

when a present reading address of a reading command is same as a previous reading address, a data is output in a data-lag mode to output said data after said response is outputted,

when said present reading address is not same as said previous reading address, said previous reading address is updated to be said present reading address and said data is output in a data-parallel mode to output said data regardless of whether or not said response is outputted.

[Claim 10] 10. The method of claim 9, further comprising:determining whether said reading command passes a cyclic redundancy check, wherein when said reading command does

not pass the cyclic redundancy check, processing of said reading command is stopped.

[Claim 11] 11. The method of claim 9, further comprising a step of reading said data based on said reading command from a memory of said memory card.

[Claim 12] 12. The method of claim 9, wherein when said reading command is a multi-block reading command, further comprising:

outputting a data in a next block of said data after outputting said data and stop outputting said data when said memory card receives a stop command.

[Claim 13] 13. The method of claim 9, wherein said memory card is a SD memory card.

[Claim 14] 14. A memory card controller for an automatically adaptive memory card, said memory card being connected to a card reader, said card reader sending a reading command to read a data from a memory of said memory card; said memory card controller characterizing: when said memory card controller receives said reading command, determining whether a present reading address is the same as a previous reading address; when said present reading address is the same as said previous reading address, outputting a response and outputting said data after said response is outputted; and when said present reading address is not the same as said previous reading address, updating said previous reading address to be said present reading address, outputting said response, and outputting said data regardless of whether or not said response is outputted.